

404195 Project Phase-II		
Credits:06		
Teaching Scheme:		Examination Scheme:
Tutorial: 6 Hrs/Week		TW: 150 Mark OR: 50 Marks
1. GroupSize The student will carry the project work individually or by a group of students. Optimum group size is in 3 students. However, if project complexity demands a maximum group size of 4 students, the committee should be convinced about such complexity and scope of the work. 2. Selection and approval of topic Topic should be related to real life application in the field of Electronics and Telecommunication OR Investigation of the latest development in a specific field of Electronics or Communication or Signal Processing OR The investigation of practical problem in manufacture and / or testing of electronics or communication equipment OR The Microprocessor / Microcontroller based applications project is preferable. OR Software development project related to VHDL, Communication, Instrumentation, Signal Processing and Agriculture Engineering with the justification for techniques used / implemented is accepted. OR Interdisciplinary projects should be encouraged. The examination will be conducted independently in respective departments. 3. Note: The group should maintain a logbook of activities. It should have entries related to the work done, problems faced, solution evolved etc., duly signed by internal and external guides. Project report must be submitted in the prescribed format only. No variation in the format will be accepted. One guide will be assigned at the most 3 project groups.		