

Date: 29/04/2023

To

The Head,

Department of Electronics and Computer Engineering

AVCOE, Sangamner.

Subject: Permission to conduct curricular/co-curricular activity in the department of Electronics and Computer Engineering.

Respected Madam,

Department of Electronics and Computer Engineering has planned to conduct a curricular/co-curricular activity in association with IEEE Circuits and System Society (CASS) Students Branch chapter.

Proposed Date of Event: **05/05/2023 to 14/05/2023**

Type of Event: **Workshop**

Title/Topic: **Circuit Design and Development**

Resource Persons: **Mr. S.P. Rahane**

So kindly give a permission to organize the event.

Thanking You,

Yours faithfully


Er. M.T. Kanawade

IEEE Coordinator

AE/OFFICE/Invitations/2022-23/

Date: 02/05/2023

To,

Mr. S.P. Rahane

Founder,

Techno Spark Centre, Sangamner

Subject-Invitation as resource person for Workshop on "Circuit Design and Development"

Dear, **Mr. S.P. Rahane**

Greetings.....!!!!

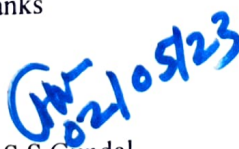
Sir, we are planning to conduct workshop on "Circuit Design and Development" on 05/05/2023 to 14/05/2023 for SE students in association with IEEE Circuits and System Society (CASS) Students Branch chapter. The primary aim of arranging such sessions is to create interest among the students about Circuit Design and Development.

Being one of the experts in Design and Development we will be grateful to you if you share your knowledge and experience with our students. Your contribution will be instrumental in keeping the pace with industry requirements and career paths of our students.

I hereby request you to accept our invitation. Kindly accept our invitation.

Looking forward to your kind acceptance.

Thanks


Dr. S.S. Gundal

H.O.D.,

Electronics and Computer Engineering

Head
Electronics & Computer Engg.
Amrutvahini COE, Sangamner



Sangamner, Maharashtra, India

H5MW+9J6, Laxmi Nagar, Sangamner, Maharashtra

422605, India

Lat 19.583398°

Long 74.196475°

12/05/23 12:06 PM GMT +05:30



Sangamner, Maharashtra, India

H5MW+9J6, Laxmi Nagar, Sangamner, Maharashtra

422605, India

Lat 19.583396°

Long 74.196475°

12/05/23 12:06 PM GMT +05:30

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HOD
Dept of Electronics Engg
TMC OE Sangamner



GPS Map Camera

Sangamner, Maharashtra, India
H5MW+9J6, Laxmi Nagar, Sangamner, Maharashtra 422605,
India
Lat 19.583434°
Long 74.196528°
13/05/23 02:32 PM GMT +05:30



Ref. AE/Elex.Dept/2022-23/

Date: 23/04/2023

To,
Mr. S.P. Rahane
Founder,
Techno Spark Centre, Sangamner

Letter of Appreciation

Dear Mr. S.P. Rahane

We are writing this letter to express our gratefulness towards you for taking pains and clearing our students doubts on "Circuit Design and Development" on date 05/05/2023 to 14/05/2023 in association with IEEE Circuits and System Society(CASS) Students Branch chapter. We truly appreciate the way you taught our students and made them ready for the future exams. You have done a great favor to our students and we have no words to express our gratitude towards you.

We assure you that your efforts will not go waste and our students will pass out with excellent marks and make you feel proud of them. Once again, thanks for all the help extended to us.

With warm regards



Dr.S.S.Gundal

Head of Department

Electronics and Computer Engineering

Amrutvahini College of Engineering, Sangamner
Department of Electronics Engineering
Workshop on Circuit Design and Development

Batch - S1

Sr. No.	Name of the Student	05/05/2023 10 to 5 am to 5 pm	06/05/2023 2.00 to 6.00 pm	07/05/2023 10.00 to 5.00 pm	12/05/2023 2.00 to 6.00 pm	13/05/2023 2.00 to 6.00 pm	14/05/2023 10.00 to 5.00 pm
1	Payal Malunekar	Payal	Payal	Payal	Payal	Payal	Payal
2	Pagare Rani Arun	Pagare R	Pagare R	Pagare R	Pagare R	Pagare R	Pagare R
3	Walamale Vaishnavi	Vaishnavi	Vaishnavi	Vaishnavi	Vaishnavi	Vaishnavi	Vaishnavi
4	Hikare Shravani	Shravani	Shravani	Shravani	Shravani	Shravani	Shravani
5	Pokale Tejal	Pokale T	Pokale T	Pokale T	Pokale T	Pokale T	Pokale T
6	Navale Shradddha	Shradddha	Shradddha	Shradddha	Shradddha	Shradddha	Shradddha
7	Navale Shradddha	Shradddha	Shradddha	Shradddha	Shradddha	Shradddha	Shradddha
8	Pansare Shradha	Shradha	Shradha	Shradha	Shradha	Shradha	Shradha
9	Pund Snehal Anil	Snehal	Snehal	Snehal	Snehal	Snehal	Snehal
10	Ambre Sakshi S.	Sakshi A.	Sakshi A.	Sakshi A.	Sakshi A.	Sakshi A.	Sakshi A.
11	Gulve Aarti Sunil	Aarti	Aarti	Aarti	Aarti	Aarti	Aarti
12	Kardile Amruta	Amruta	Amruta	Amruta	Amruta	Amruta	Amruta
13	Deshmukh Snehal	Snehal	Snehal	Snehal	Snehal	Snehal	Snehal
14	Pagare Mansi	Mansi	Mansi	Mansi	Mansi	Mansi	Mansi
15	Pathade Pranali K.	Pranali	Pranali	Pranali	Pranali	Pranali	Pranali
16	Varpe Gauravi N	Gauravi	Gauravi	Gauravi	Gauravi	Gauravi	Gauravi
17	Anbhule Sidharth	Anbhule	Anbhule	Anbhule	Anbhule	Anbhule	Anbhule
18							
19							
20							
21							
22							
23							

Er. M.T. Kanawde

Workshop Co-ordinator

Dr. S.S. Gundal
H.O.D.

Electronics & Computer Engg.
Amrutvahini COE, Sangamner

Amrutvahini College of Engineering, Sangamner
Department of Electronics Engineering
Workshop on Circuit Design and Development

Batch - S2

Sr. No.	Name of the Student	05/05/2023 10.00am to 5.00 pm	06/05/2023 2.00 to 6.00 pm	07/05/2023 10.00 to 5.00pm	12/05/2023 2.00 to 6.00pm	13/05/2023 2.00 to 6.00pm	14/05/2023 10.00 to 5.00 pm
24	Ambilwade Saurabh	<u>Ambilwade</u>	<u>Ambilwade</u>	<u>Ambilwade</u>	<u>Ambilwade</u>	<u>Ambilwade</u>	<u>Ambilwade</u>
25	Shirsam Prathmesh	<u>Shirsam</u>	<u>Shirsam</u>	<u>Shirsam</u>	<u>Shirsam</u>	<u>Shirsam</u>	<u>Shirsam</u>
26	Chaskar Samket	<u>Chaskar</u>	<u>Chaskar</u>	<u>Chaskar</u>	<u>Chaskar</u>	<u>Chaskar</u>	<u>Chaskar</u>
27	Badakh Sakshi	<u>Badakh</u>	<u>Badakh</u>	<u>Badakh</u>	<u>Badakh</u>	<u>Badakh</u>	<u>Badakh</u>
28	Bhagade Harshada	<u>Bhagade</u>	<u>Bhagade</u>	<u>Bhagade</u>	<u>Bhagade</u>	<u>Bhagade</u>	<u>Bhagade</u>
29	Nagare Samrudhis	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>
30	Nipunge sakshi P	<u>Nipunge</u>	<u>Nipunge</u>	<u>Nipunge</u>	<u>Nipunge</u>	<u>Nipunge</u>	<u>Nipunge</u>
31	Dengale pranavi G	<u>Dengale</u>	<u>Dengale</u>	<u>Dengale</u>	<u>Dengale</u>	<u>Dengale</u>	<u>Dengale</u>
32	Deshmukh Shruti S	<u>Deshmukh</u>	<u>Deshmukh</u>	<u>Deshmukh</u>	<u>Deshmukh</u>	<u>Deshmukh</u>	<u>Deshmukh</u>
33	Vishwambhar Sakshi	<u>Vishwambhar</u>	<u>Vishwambhar</u>	<u>Vishwambhar</u>	<u>Vishwambhar</u>	<u>Vishwambhar</u>	<u>Vishwambhar</u>
34	Thorat Prachi	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>
35	Chaudharitar Laxmi	<u>Chaudharitar</u>	<u>Chaudharitar</u>	<u>Chaudharitar</u>	<u>Chaudharitar</u>	<u>Chaudharitar</u>	<u>Chaudharitar</u>
36	Shendge Pratiksha	<u>Shendge</u>	<u>Shendge</u>	<u>Shendge</u>	<u>Shendge</u>	<u>Shendge</u>	<u>Shendge</u>
37	Nagare Sakshi	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>	<u>Nagare</u>
38	Thorat Shivani	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>	<u>Thorat</u>
39	Gore Deepthi	<u>Gore</u>	<u>Gore</u>	<u>Gore</u>	<u>Gore</u>	<u>Gore</u>	<u>Gore</u>
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46							

Er. M.T. Kanawde
Workshop Co-ordinator

Dr. S.S. Gandal

Head of Department
Electronics & Computer Engg.
Amrutvahini COE, Sangamner

Amrutvahini College of Engineering, Sangamner
Department of Electronics Engineering
Workshop on Circuit Design and Development

Batch - 23

Sr. No.	Name of the Student	05/05/2023 10:00 am to 5:00 pm	06/05/2023 2:00 to 6:00 pm	07/05/2023 10:00 to 5:00 pm	12/05/2023 10:00 am to 5:00 pm	13/05/2023 2:00 to 6:00 pm	14/05/2023 10:00 to 5:00 pm
47	More Pritesh	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
48	YADAV ARYAN	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
49	Shete Parikshit	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
50	Chavhan Akshay	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
51	Patel Nikhil	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
52	Shinde Rushikesh	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
53	Rasal Sanket	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
54	Sanap Omkar	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
55	Ugale Nikhil	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
56	Chaudhari Tushar	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
57	Dube Yashraj	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
58	Vishwambhar Vedant	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
59							
60	Tambe Sindhuja	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
61							
62	Thorat Rushikesh	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
63							
64							
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66							
67	Vishwambhar Vedant	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
68							
69	Sonawane Saurabh	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>

Er. M.T. Kanawde
Workshop Co-ordinator

Dr. S.S. Gundal

H.O.D.
Head

Electronics & Computer Engg.
Amrutvahini COE, Sangamner

Amrutvahini College of Engineering, Sangamner
Department of Electronics Engineering
Workshop on Circuit Design and Development

Batch-84

Sr. No.	Name of the Student	05/05/2023 10:00 to 5:00 Pm	06/05/2023 2:00 to 6:00pm	07/05/2023 10:00 to 5:00pm	12/05/2023 2 to 6:00pm	13/05/2023 2 to 6:00pm	14/05/2023 10:00 to 5:00pm
70	Chavan Jayesh	J.Chavan	J.Chavan	J.Chavan	J.Chavan	J.Chavan	J.Chavan
71	Bhaskar Mayuresh	Bhaskar	Bhaskar	Bhaskar	Bhaskar	Bhaskar	Bhaskar
72	Bhurala Pritam	Pri	Pri	Pri	Pri	Pri	Pri
73	Patil Prathamsingh	Patil	Patil	Patil	Patil	Patil	Patil
74	Kokate Samir	Samir	Samir	Samir	Samir	Samir	Samir
75	Rothe Aniket	Aniket	Aniket	Aniket	Aniket	Aniket	Aniket
76	Rahane Prathmesh	Rahane	Rahane	Rahane	Rahane	Rahane	Rahane
77	Kshirsagar Saranuddhi	Saranuddhi	Saranuddhi	Saranuddhi	Saranuddhi	Saranuddhi	Saranuddhi
78	Kale Aditi	Aditi	Aditi	Aditi	Aditi	Aditi	Aditi
79	Kadlag Nikhil	Nikhil	Nikhil	Nikhil	Nikhil	Nikhil	Nikhil
80	Kanawade Nishant	Nishant	Nishant	Nishant	Nishant	Nishant	Nishant
81	Sahane Arun	Arun	Arun	Arun	Arun	Arun	Arun
82	Gore Prem	Prem	Prem	Prem	Prem	Prem	Prem
83	Gude vedant	Vedant	Vedant	Vedant	Vedant	Vedant	Vedant
84	Bhonde Sandesh	Sandesh	Sandesh	Sandesh	Sandesh	Sandesh	Sandesh
85	Muntade Karan	Karan	Karan	Karan	Karan	Karan	Karan
86	Sonawane Shreyas	Shreyas	Shreyas	Shreyas	Shreyas	Shreyas	Shreyas
87	Gomase Gayatri	Gayatri	Gayatri	Gayatri	Gayatri	Gayatri	Gayatri
88	Gosavi Maithilee	Maithilee	Maithilee	Maithilee	Maithilee	Maithilee	Maithilee
89	Greenjal Shraddha	Shraddha	Shraddha	Shraddha	Shraddha	Shraddha	Shraddha
90	Kangane Yashali	Yashali	Yashali	Yashali	Yashali	Yashali	Yashali
Total = 70							

Er. M.T. Kanawde
Workshop Co-ordinator

Dr. S.S. Gundal
H.O.D.
Head
Electronics & Computer Engg.
Amrutvahini COE, Sangamner

Date: 24/04/2023

To,

The Principal,

AVCOE, Sangamner

Subject: Report of Workshop on "Circuit Design and Development"

Respected Sir,

Electronics and Computer Department conducted Two week Workshop on "Circuit Design and Development" for ~~2nd~~ Year students of Electronics Engineering from 05/05/2023 to 14/05/2023. It is organized by Electronics and Computer Department. We are herewith submitting the report for the same.

Name of Resource Person : Mr. S.P. Rahane,

Founder, Techno Spark Centre, Sangamner

Subject : Circuit Design and Development

Targeted students : Second Year students of Electronics and Computer Engineering

Number of Students : 70

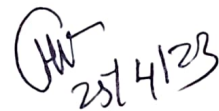
This is for your kind information

Thanking you



Er. M.T. Kanawade

IEEE Coordinator



Dr. S. S. Gundal

Head of Department

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