



Certificate



This is to certify that **DR.SANDIP BHAGWAT RAHANE** has successfully completed the AICTE-ISTE approved Orientation/Refresher Programme on “**Low Power VLSI System Design**” held during **10.01.2022** to **15.01.2022** organized by **Walchand Institute of Technology, Solapur, Maharashtra.**

Director (FDC)
AICTE, ND

Executive Secretary
ISTE, ND

Program Coordinator/Principal
WIT, Solapur