



Amrutvahini College of Engineering, Sangamner
Department of Electronics and Telecommunication

Organizes
A Guest Lecture on

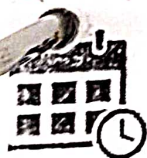
Career Opportunities in VLSI Design Engineering



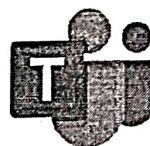
Speaker:

Mr. Ganesh Lande

**Application Engineer II,
Synopsys Indian Pvt. Ltd., Bangalore**



Saturday, 09/10/2021
11.00 am to 1.00 pm



On Microsoft Teams

: C. D. Bhos & Mr. A. E. Kachare
(Coordinators)

Dr. R. P. Labade
(HOD, E&TC)

Dr. M. A. Venkatesh
(Principal)



HOD E & Tc <hod.entc@avcoe.org>

Invitation for Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students on 09th Oct., 2021.

Message

HOD E & Tc <hod.entc@avcoe.org>
: glande57@gmail.com

Wed, Oct 6, 2021 at 2:55 PM

Dear Ganesh,

Warm greetings from Amrutvahini College of Engineering, Sangamner.

Hope you are doing well.

We wish to invite you for online Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students of our college on 09th Oct., 2021 to share your valuable knowledge and experience in this domain.

We are very much sure that this session will definitely help students in developing their future career. We shall be much obliged to you, if you kindly accept the proposal.

Please find the attachment of the Invitation letter.

—
Thanks and Regards,
Dr.Ms.Rekha P Labade
IEEE Student's Branch Counselor
Professor and Head
Electronics and Telecommunication
Amrutvahini College of Engineering
Mobile No.9850498510

"Fragrance always remains in the hands of those who distribute Roses"

 Invitation.doc
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Amrutvahini College of Engineering, Sangamner
Department of Electronics & Telecommunication

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Ref/AE/ETC/2021-22/

Date: 06/10/2021

To,

Mr. Ganesh Lande,

Application Engineer II,

Synopsys Indian Pvt. Ltd., Bangalore.

Subject: Invitation for Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students on 09th Oct., 2021.

Dear Sir,

Warm greetings from Amrutvahini College of Engineering, Sangamner.

We are one of the well-known institutes affiliated to Savitribai Phule, Pune University. The institute regularly organizes Workshops/STTP's/Guest Lectures/Seminars, etc throughout the academic year.

We wish to invite you for online Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students of our college on 09th Oct., 2021 to share your valuable knowledge and experience in this domain.

We are very much sure that this session will definitely help students in developing their future career. We shall be much obliged to you, if you kindly accept the proposal.

Thanking you,

Regards.



Dr. Ms. R. P. Labade

HOD, E&TC

Dept. Of Electronics & Telecom. Engg,
Amrutvahini College Of Engineering,
Sangamner, Dist.-Ahmednagar(MS)

Amrutvahini College of Engineering, Sangamner
Department of Electronics & Telecommunication

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Ref/AE/ETC/2021-22/ 7174

Date: 09/10/2021

To,

Mr. Ganesh Lande,

Application Engineer II,

Synopsys Indian Pvt. Ltd., Bangalore.

Subject: Expression of Gratitude.

Dear Sir,

We sincerely wish to thank you for readily accepting our invitation and conducting an online Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students of Amrutvahini College of Engineering, Sangamner on 09th Oct., 2021.

Your session was well received by students and they were highly benefited with your experience and knowledge-based inputs. Your valuable exposure about the skill-set required for VLSI domain will definitely help the students to achieve their highest watermark in the domain.

We thank you once again and look forward the same favorable support in future.

Thanking you,

Regards.



Dr. Ms. R. P. Labade

HOD, E&TC

Department of Electronics & Telecom. Engg,
Amrutvahini College Of Engineering,
Sangamner, Dist.-Ahmednagar(MS)

Amrutvahini College of Engineering, Sangamner
Department of Electronics & Telecommunication

Ref/AE/ETC/2021-22/ 7175

Date: 09/10/2021

To,
The Principal,
AVCOE, Sangamner.

Subject: Conduction Report of an online Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students on 09th Oct., 2021.

Respected Sir,

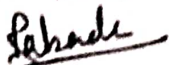
An online Guest Lecture on "Career Opportunities in VLSI Design Engineering" for E&TC students was conducted on 09th Oct., 2021.

The details of the session are as follows:

- Resource person : Mr. Ganesh Lande,
Application Engineer II,
Synopsys Indian Pvt. Ltd., Bangalore.
- Total no. of participants : 55 (SE, TE, and BE E&TC students)
- Objectives :
1. To explore the knowledge of students in VLSI design domain.
2. To make students familiar with career opportunities in VLSI design domain.
- Outcomes :
1. Students got aware of hardware and software skill-sets required for VLSI design.
2. They came to know about the challenges faced by VLSI design engineer in the industrial projects.
3. They got the insights about various job opportunities in VLSI design.
- Coordinator : Mr. C. D. Bhos and Mr. A. E. Kachare

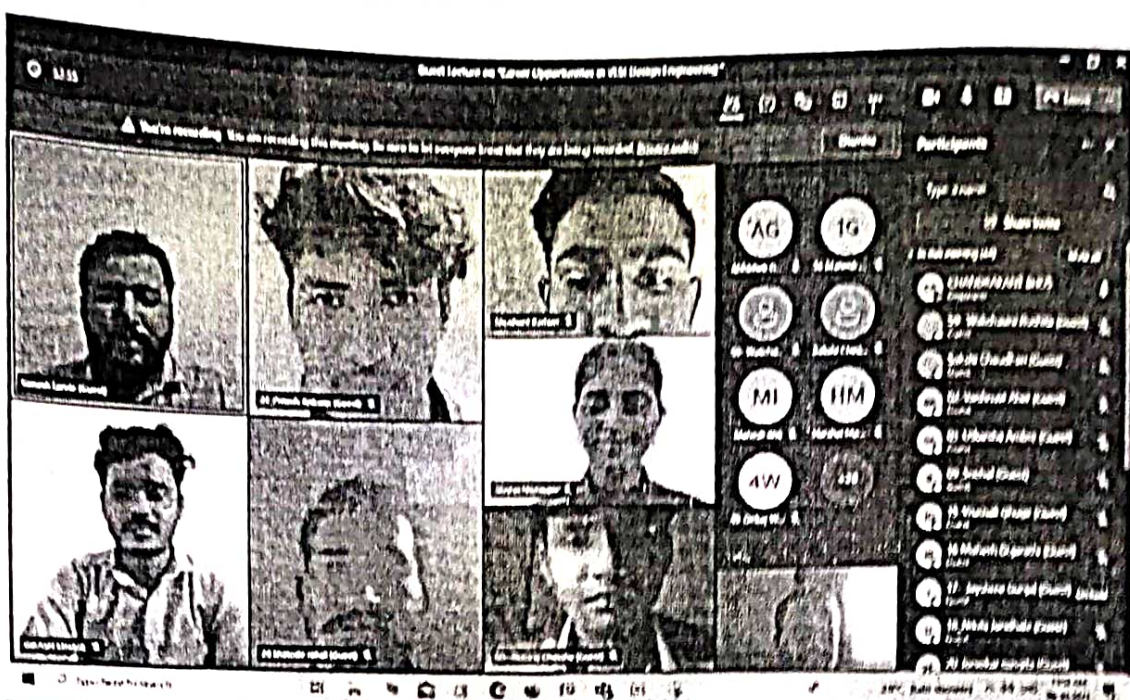
This is for your kind information.
Thanking you,

With Regards,

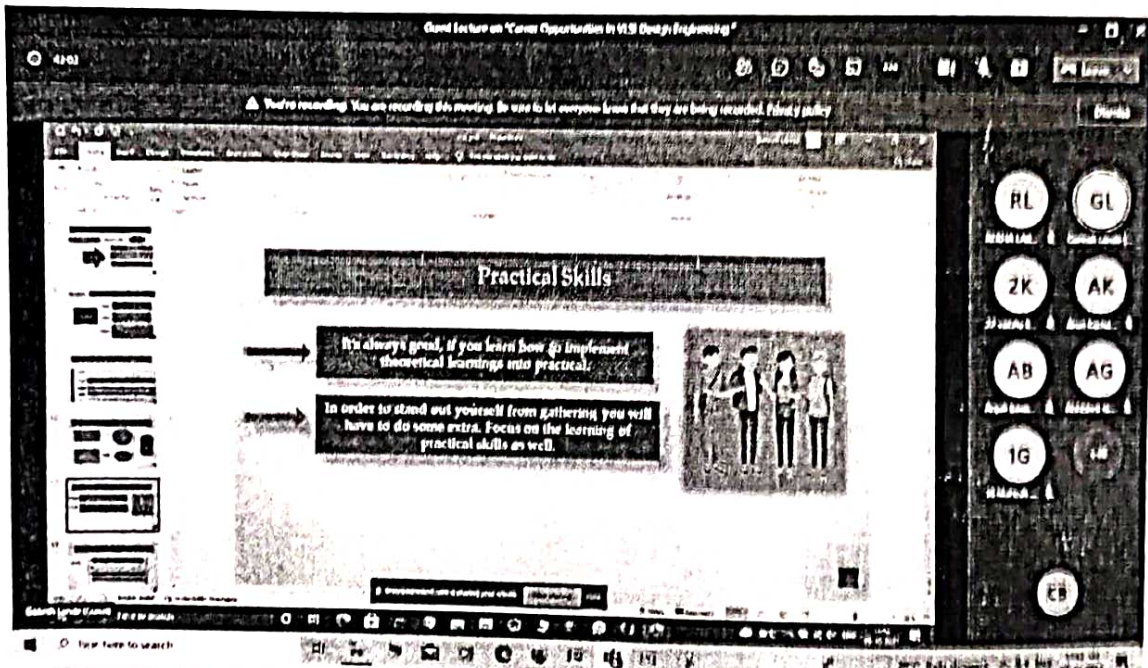


Dr. M. S. R. P. Labade

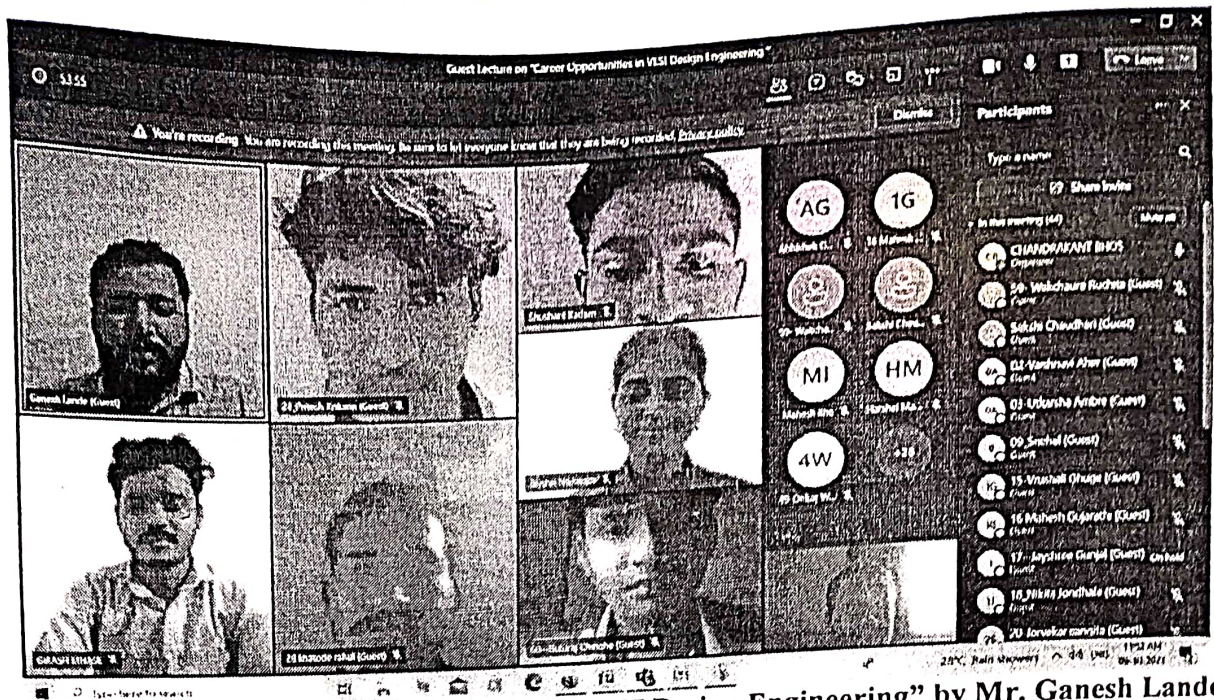
UOP, HOD, E&TC
Dept. Of Electronics & Telecom. Engg
Amrutvahini College Of Engineering,
Sangamner, Dist.-Ahmednagar(MS)



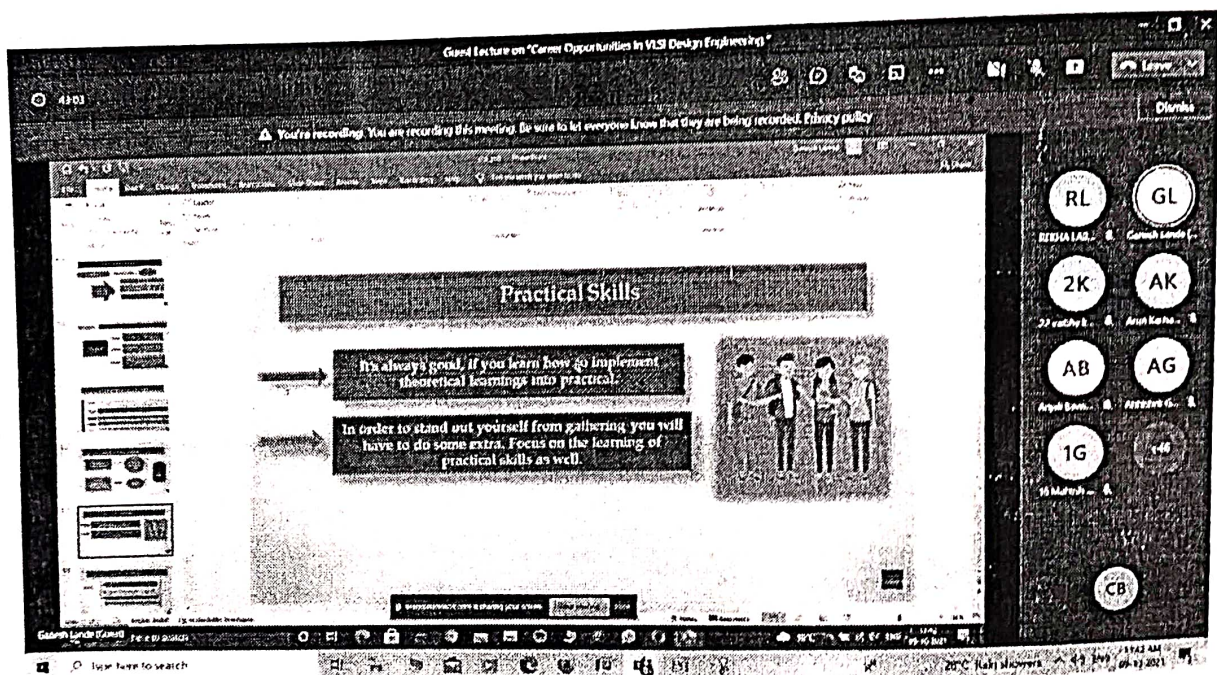
Guest Lecture on "Career Opportunities in VLSI Design Engineering" by Mr. Ganesh Lande



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