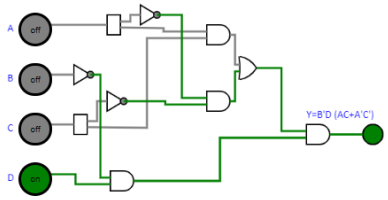
Electronic lock using basic logic gates

Simulation



Task: Design a three input electronic lock that operates when A = 1, B = 0, and C = 0. Construct and verify using simulator.